

REMARKS

I. STATUS OF CLAIMS

In the Office Action mailed December 14, 2005, the Examiner allowed claims 15-23, objected to claim 3, and rejected claims 1, 2, and 4-14. Claims 1, 6, 7 and 14 are amended herein. No new matter has been added.

Thus, it is respectfully submitted that claims 1-23 remain pending and under consideration in this present application.

II. REJECTIONS UNDER 35 U.S.C. § 102

In item 6 on pages 4-6 of the Office Action, claims 1, 2, 4-7 and 14 were rejected under 35 U.S.C. § 102(b) as anticipated by U.S. Patent 4,439,864 to Qureshi.

Claim 1 as amended specifically recites, amongst other novel features, "**converting the analog signal to a digital signal via a first path**", "**performing an arithmetic processing of the digital signal via the first path**", "**delaying the analog signal ... in a second path** that is different from the first path", and "**controlling the delayed analog signal** in accordance with the control signal in the second path". (Emphasis added). Claims 6, 7 and 14 recite somewhat similar features. Further, claim 14 also specifically recites, amongst other novel features, "**a first control loop, located in a first path**", "**a delay circuit, located in a second path that is different from the first path**", and "**a gain control amplifier (GCA), located in the second path**". It is respectfully submitted that Qureshi fails to disclose, teach or suggest these features.

In item 4 on pages 2-3, and item 6 on page 4 of the current Office Action, it appears that the Examiner believes that the configuration and function of the band pass filter 60 in FIG. 2 of Qureshi corresponds to the recitation of "delaying the analog signal" as recited in Applicant's claimed invention in, for example, claim 1. However, Applicant's respectfully submit that a review of FIG. 2 of Qureshi, and a complete reading of Qureshi reveals that Qureshi fails to disclose, teach or suggest Applicants claimed invention as recited in claim 1, for example.

Instead, Qureshi discloses how its **band pass filter 60** is provided at a preceding stage of an A/D converter 64 **in the same path** as the A/D converter 64. See, for example, FIG. 2 of Qureshi. Further, in FIGS. 2 and 7 of Qureshi, **delay circuits 238 and 240** of a signal processor element 36 are provided at a subsequent stage of the A/D converter 64 **in the same path** as the A/D converter 64. Further, in FIG. 3 of Qureshi, a **carrier detect circuit 151** is provided at a

subsequent stage of the A/D converter 64 **in the same path** as the A/D converter 64.

In contrast, Applicant specifically recites two different paths: "a first path" and "a second path". Therefore, Applicants invention provides "converting the analog signal to a digital signal via **a first path**", "performing an arithmetic processing of the digital signal via the first path", "delaying the analog signal ... **in a second path that is different from the first path**", and "controlling the delayed analog signal in accordance with the control signal in the **second path**". Therefore, the configuration of Applicant's claimed invention which provides a first path, and a second path that is different from the first path, is absent in Qureshi.

In view of the above, it is respectfully submitted that the rejection is overcome.

Although the above comments are specifically directed to claim 1, it is respectfully submitted that the comments would be helpful in understanding differences in the claims 2, 4-7 and 14 over the cited references.

III. REJECTIONS UNDER 35 U.S.C. § 103(a)

In items 7-10 on pages 6-9 of the Office Action, claims 8-13 were rejected as unpatentable over various combinations of Qureshi as a primary reference with U.S. Patent 6,563,859 to Oishi et al; and U.S. Patent 6,897,724 to Gurvich. Nothing was cited or has been found in any of these additional references suggesting modification of Qureshi to overcome the deficiencies discussed above. Since claims 8-13 depend from claim 7, it is submitted that claims 8-13 patentably distinguish over Qureshi for the reasons discussed above with respect to claims 1, 6, 7 and 14.

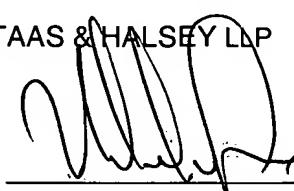
IV. CONCLUSION

There being no further outstanding objections or rejections, it is submitted that the application is in condition for allowance. An early action to that effect is courteously solicited.

If there are any additional fees associated with filing of this Amendment, please charge the same to our Deposit Account No. 19-3935.

Respectfully submitted,

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